

FIG. 1

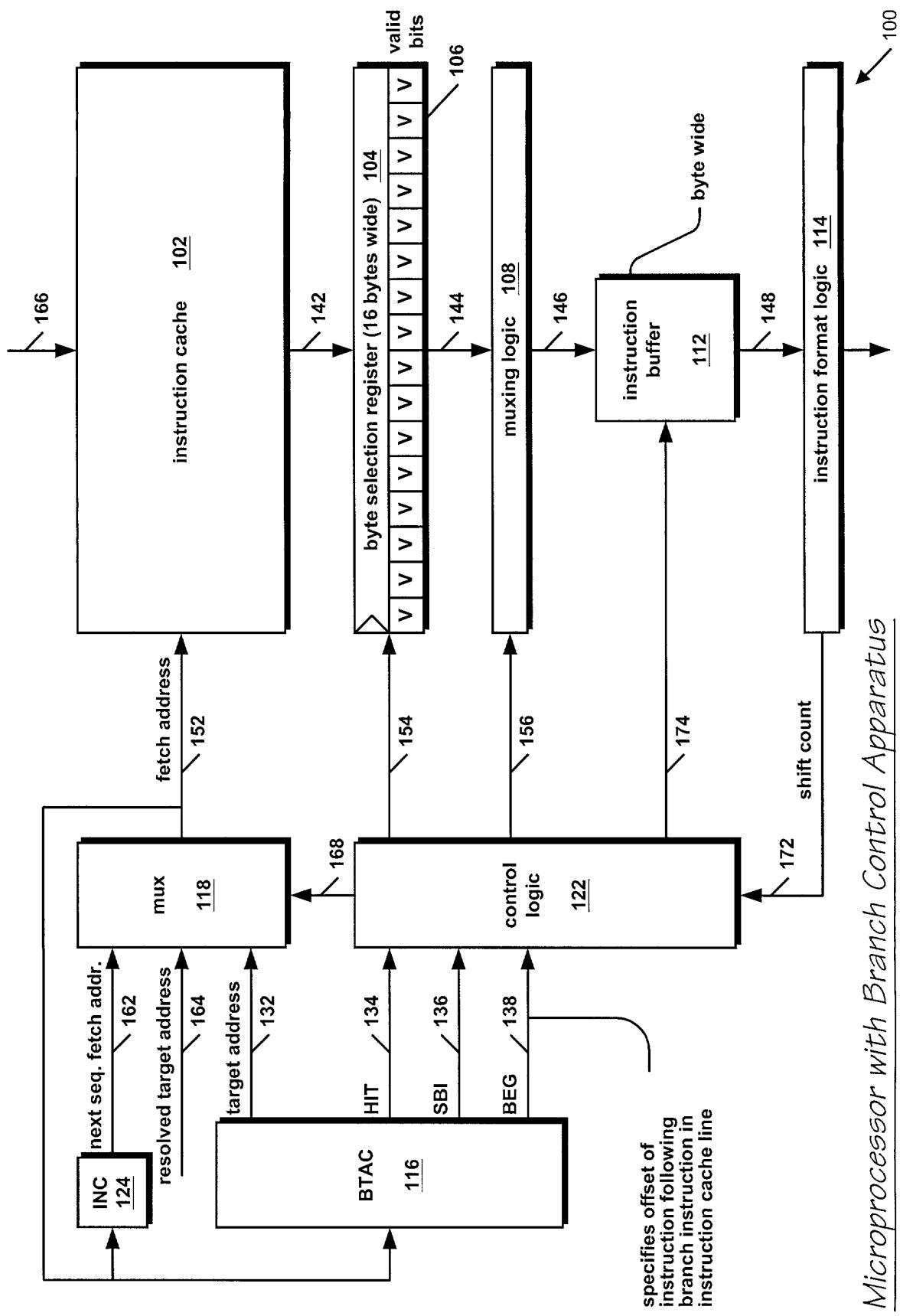


FIG. 2

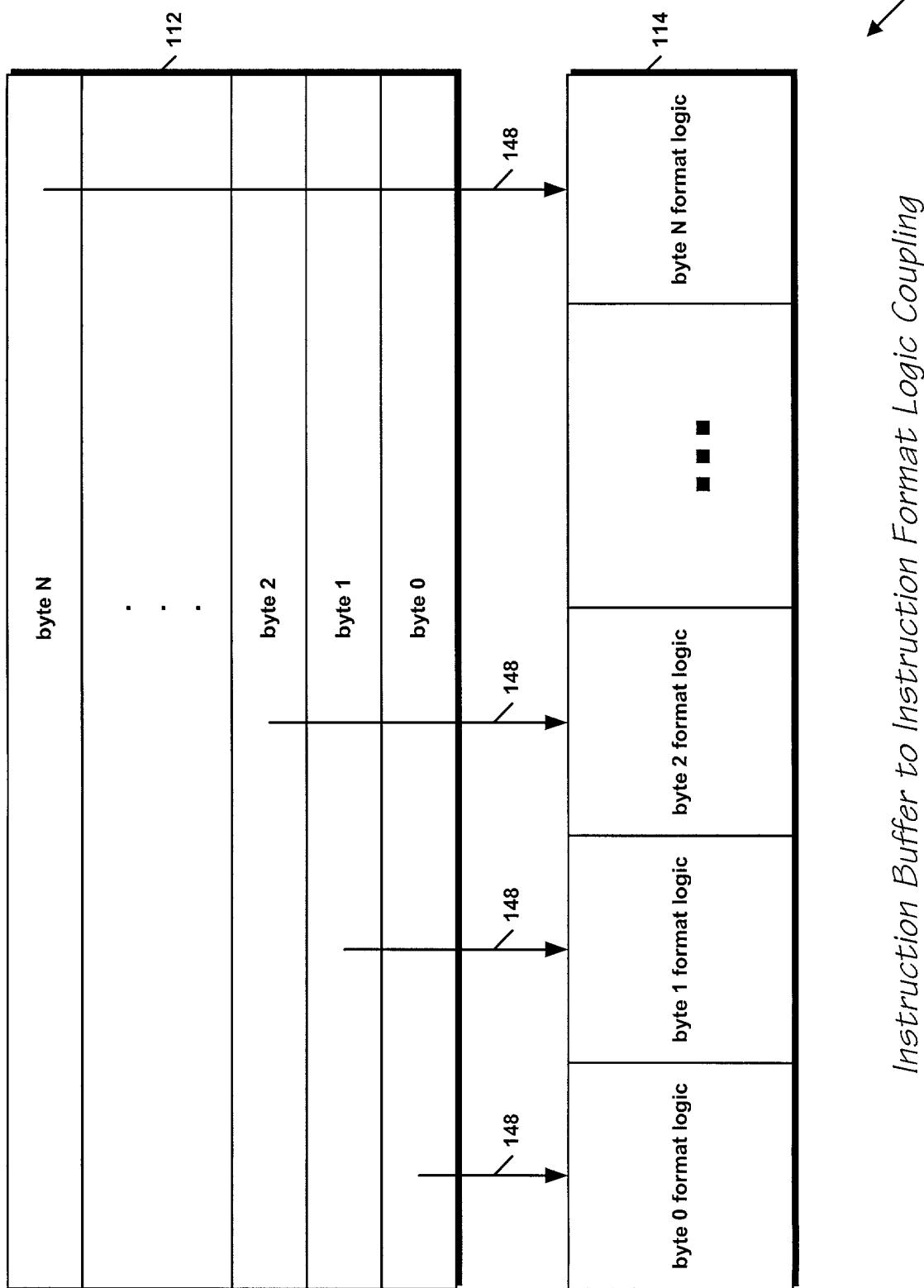


FIG. 3

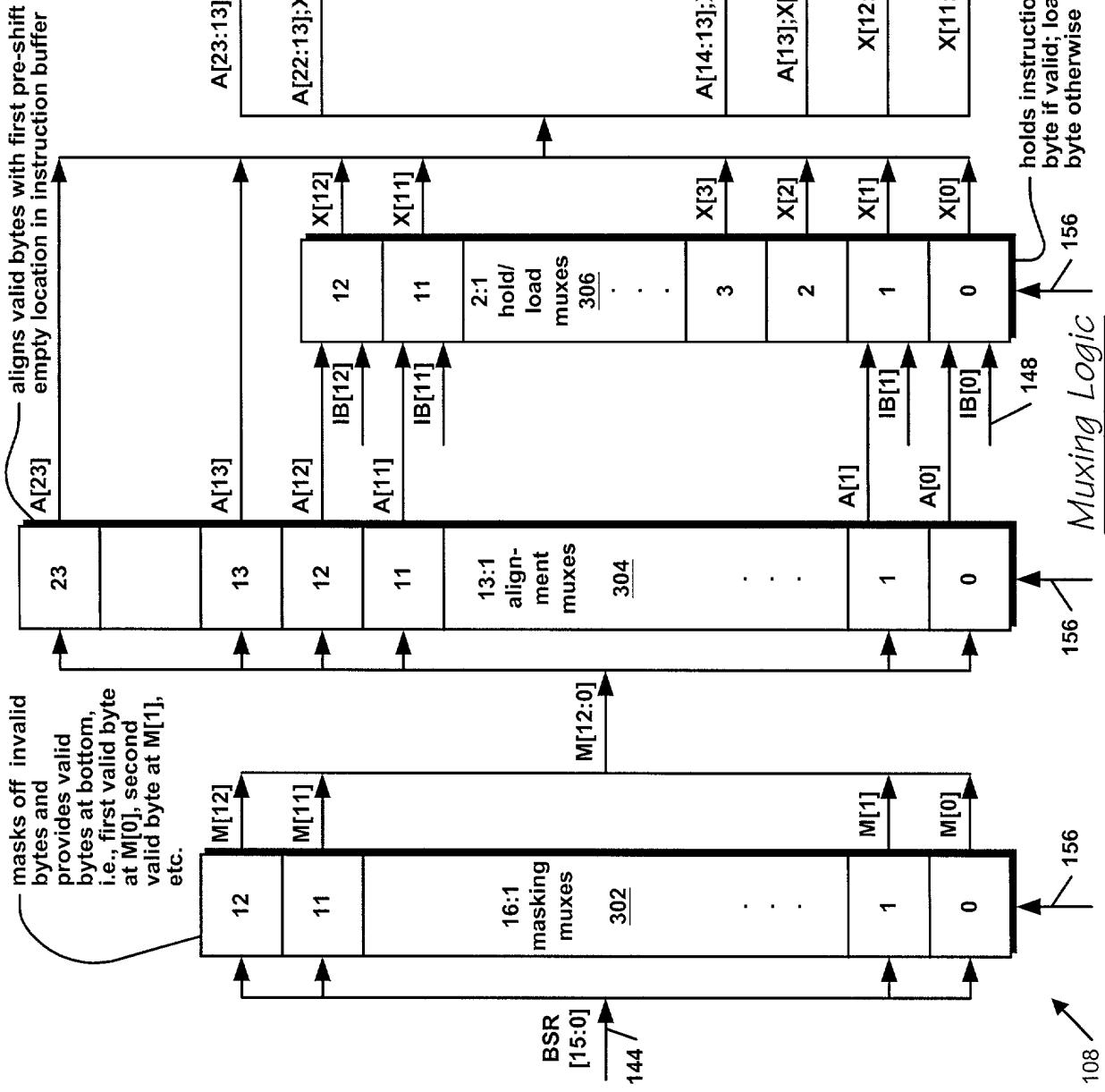


FIG. 4

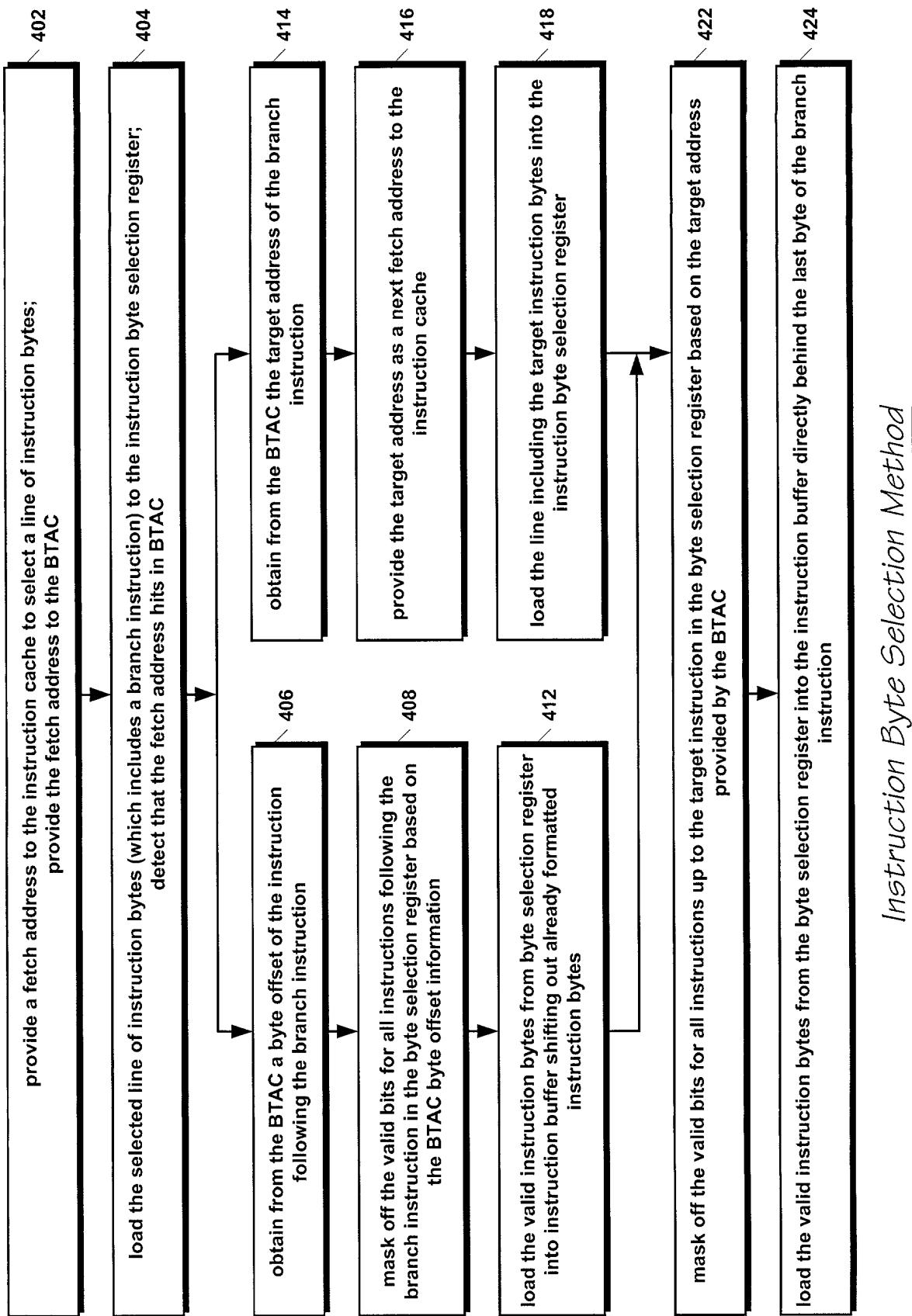
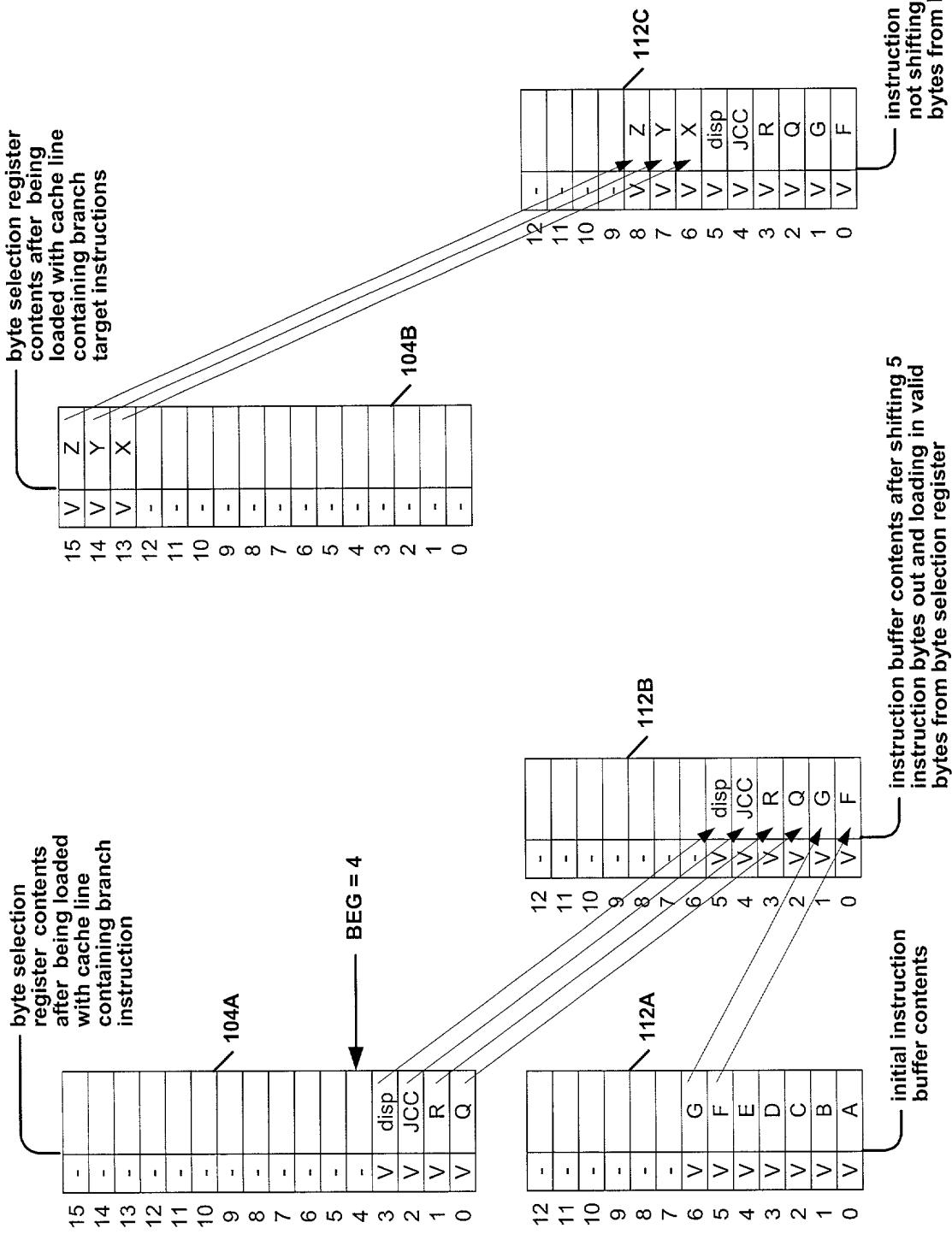


FIG. 5



Branch Control Apparatus Operation Example

instruction buffer contents after not shifting and loading in valid bytes from byte selection register

instruction buffer contents after shifting 5 instruction bytes out and loading in valid bytes from byte selection register